

HIGH LEVEL SYNTHESIS

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High-level synthesis - Wikipedia

High-level synthesis (HLS), sometimes referred to as C synthesis, electronic system-level (ESL) synthesis, algorithmic synthesis, or behavioral synthesis, is an automated design process that interprets an algorithmic description of a desired behavior and creates digital hardware that implements that behavior.

Vivado High-Level Synthesis tool. - Xilinx

Advanced algorithms used today in wireless, medical, defense, and consumer applications are more sophisticated than ever before. Vivado High-Level Synthesis included as a no cost upgrade in all Vivado HLX Editions, accelerates IP creation by enabling C, C++ and System C specifications to be directly targeted into Xilinx programmable devices.

Intel High Level Synthesis Compiler: User Guide

High Level Design Report Layout Reviewing the Report Summary Reviewing Loop Information The Intel High Level Synthesis (HLS) Compiler parses your design, compiles it to an x86-64 object or FPGA-targeted RTL code, and creates an executable testbench. Running the executable file runs your testbench.

high level - LEO: bersetzung im Englisch-Deutsch-Wörterbuch

junior high / junior high school - Realschule

Letzter Beitrag: 08. Jan. 11, 02:57 Die Probleme bei der bersetzung unterschiedlicher Schultypen sind hinlänglich bekannt.

Stratus High-Level Synthesis - Cadence

The first high-level synthesis platform for use across your entire SoC design, Cadence Stratus High-Level Synthesis (HLS) delivers up to 10X better productivity than traditional RTL design.

High-Level Synthesis - Mentor Graphics

The Catapult High-Level Synthesis Platform empowers designers to use industry-standard ANSI C++ and SystemC to describe functional intent and move up to a more productive abstraction level.

Vivado Design Suite Users Guide: High Level Synthesis - Xilinx

High-Level Synthesis www.xilinx.com 2 UG902

(v2014.1) May 30, 2014 Revision History The following table shows the revision history for this document. Date Version Revision 04/02/2014 2014.1 Updated Vivado Design Suite User Guide: High-Level Synthesis content organization and added the new HLS UltraFast Design Methodology section.

High-Level Synthesis with LegUp

LegUp is an open source high-level synthesis tool being

developed at the University of Toronto. The LegUp framework allows researchers to improve C to Verilog synthesis without building an infrastructure from scratch.
high-level - LEO; bersetzung im Englisch Deutsch W rterbuch

Lernen Sie die bersetzung f r 'high-level' in LEOs Englisch Deutsch W rterbuch. Mit Flexionstabellen der verschiedenen F lle und Zeiten Aussprache und relevante Diskussionen Kostenloser Vokabeltrainer

high level - Deutsch- bersetzung Linguee W rterbuch

Viele bersetzte Beispiels tze mit "high level" Deutsch-Englisch W rterbuch und Suchmaschine f r Millionen von Deutsch- bersetzungen.

dict.cc W rterbuch :: high-level :: Englisch-Deutsch ...

high level Hochlage {f} High-Pegel {m}spec. high level group Ausschuss {m}EU high-level installation

Hochregallager {n} [funktional] high-level language

Hochsprache {f}comp. h here Programmiersprache {f}

comp. high-level languages Hochsprachen {pl} comp.

high-level officials hochrangige Beamte {pl}admin. high-

level rack Hochregal {n}ind.

High Level Structure - ISO 9001:2015 Struktur und Aufbau ...

Die High Level Structure wird wahrscheinlich auch in die Revision der ISO/TS 16949 kommen, die voraussichtlich ab Herbst 2016 die ISO 9001:2015 als Basis Norm einf hrt und die alte Ausgabe der 2008 Version abl st.

Unternehmen sind aber nicht daran gebunden die HLS in Ihr Qualit tsmanagement zu bertragen.

High-Level Synthesis and RTL Low-Power - Mentor Graphics

High-Level Synthesis. The Catapult High-Level Synthesis (HLS) Platform is the industry s leading HLS platform with proven quality results and 25-50 percent reductions in verification cost.

SPARK: High-Level Synthesis using Parallelizing Compiler ...

SPARK: A Parallelizing Approach to the High-Level Synthesis of Digital Circuits Overview SPARK is a C-to-VHDL high-level synthesis framework that employs a set of innovative compiler, parallelizing compiler, and synthesis transformations to improve the quality of high-level synthesis results.