

CARBON NANOTUBE BASED VLSI INTERCONNECTS%0A

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The brief primarily focuses on the performance analysis of CNT based interconnects in current research scenario. Different CNT structures are modeled on the basis of transmission line theory. Performance comparison for different CNT structures illustrates that CNTs are more promising than Cu or

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Multi-walled carbon nanotubes (MWNT) have provided potentially attractive solution over single-wall carbon nanotube (SWNT) bundles at current very large scale integration (VLSI) technologies.

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For carbon-based horizontal interconnects, there are three available processes; the direct horizontal growth of carbon nanotubes, the tipping down of vertically oriented carbon nanotubes, or the catalytic growth of multilayer graphene.

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Are Carbon Nanotubes the Future of VLSI Interconnections? Kaustav Banerjee and Navin Srivastava Electrical and Computer Engineering, University of California, Santa Barbara, CA 93106

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Future VLSI interconnects: optical ber or carbon nanotube a review Brajesh Kumar Kaushik, Saurabh Goel and Gaurav Rauthan Department of Electronics and Electrical Engineering, G.B. Pant Engineering College, Uttarakhand, India

Synthesis of carbon nanotubes and graphene for VLSI

For carbon-based horizontal interconnects, there are three available processes; the direct horizontal growth of carbon nanotubes, the flipping down of vertically oriented carbon nanotubes, or the catalytic growth of multilayer graphene. Graphene CVD should adopt lower temperature catalysts such as Ni or Co alloys for this application. It is emphasised that the growth methods must be compatible with integration.

Carbon nanotubes in interconnects - Wikipedia

Thanks to the measured properties of individual carbon nanotubes (CNTs), such material has been proposed as future material for interconnects. Particularly their current carrying capabilities are extremely high typically around 10^9 Acm^{-2} and they exhibit a ballistic length up to micrometers.